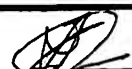
 <p>Substitute for form 1449B/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(use as many sheets as necessary)</p>		Complete if Known	
		Application Number	10/608,544
		Filing Date	June 30, 2003
		First Named Inventor	PARAMESH, Jeyanandh
		Group Art Unit	Not yet assigned
		Examiner Name	Not yet assigned
Sheet 1 of 1	Attorney Docket Number	P-5483-US	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (where appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DL	1	Ellinger, et al., "An Antenna Diversity MMIC Vector Modulator for HIPERLAN with Low Power Consumption and Calibration Capability", IEEE Transactions on Microwave Theory and Techniques, vol. 49, no. 5, May 2001.	<input type="checkbox"/>
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Examiner Signature		Date Considered	11/18/2005
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NOV 16 2004

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known

Application Number	10/608,544
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Filing Date	June 30, 2003
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First Named Inventor	PARAMESH, Jeyanandh
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Group Art Unit	2681
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Examiner Name	Not yet known
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Sheet	1	of	2	Attorney Docket Number	P-5483-US
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U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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**Examiner
Signature**

Date
Considered

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		Application Number	10/608,544		
		Filing Date	June 30, 2003		
		First Named Inventor	PARAMESH, Jeyanandh		
		Group Art Unit	2681		
		Examiner Name	Not yet known		
Sheet	2	of	2	Attorney Docket Number	P-5483-US

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DL	E	International Search Report for PCT/US2004/017427, mailed October 28, 2004	<input type="checkbox"/>
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Examiner Signature		Date Considered	
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